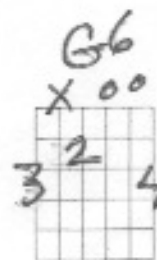
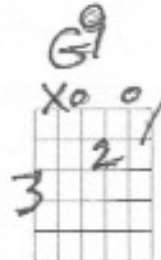
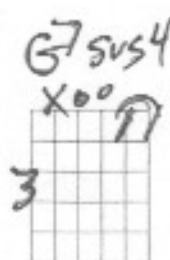
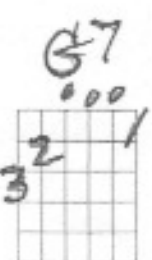
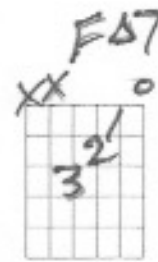
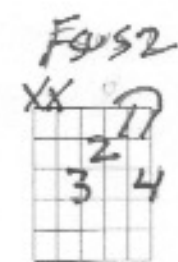
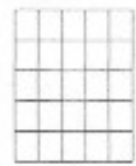
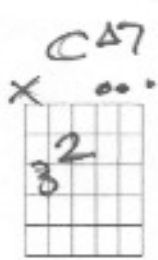
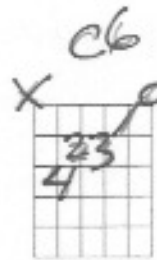
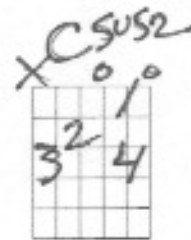
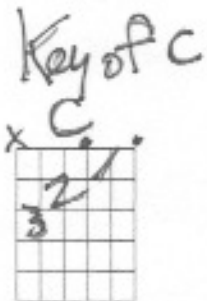
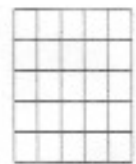
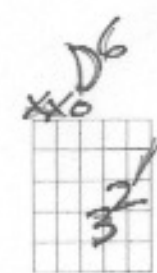
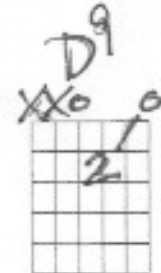
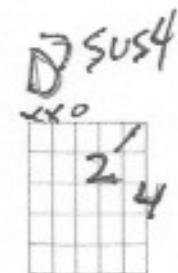
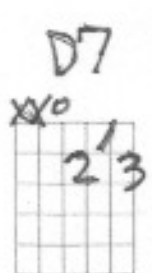
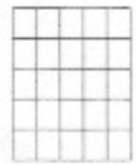
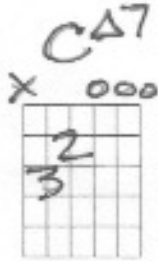
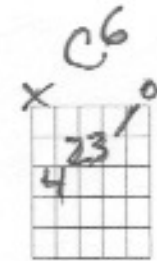
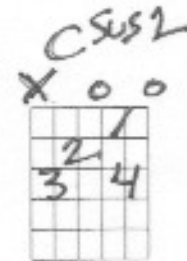
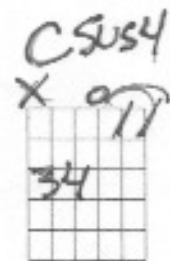
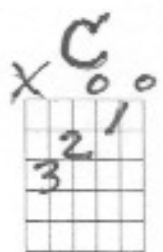
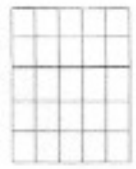
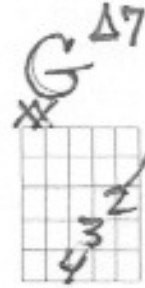
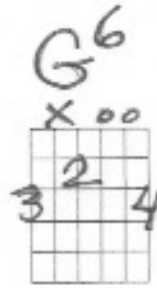
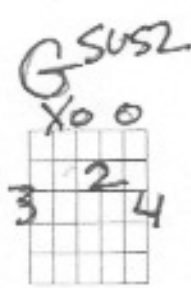
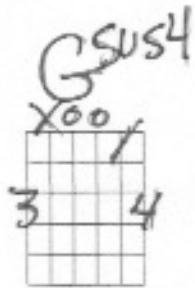
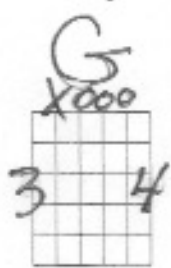


# Key of F

# Embellishment



# Key of D

D XX0 	DSUS4 XX0 	DSUS2 XX0 0 	D6 XX0 	DΔ7 XX0 	
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G X000 	GSUS4 X00 	GSUS2 X00 	G6 X00 	GΔ7 XX 	
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A7 X000 	ASUS4 X000 	A9 X0000 	A6 X0 	AΔ7 X0 	
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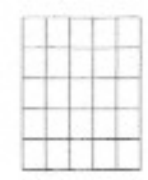
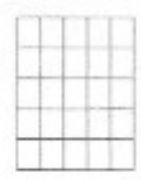
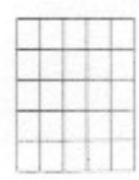
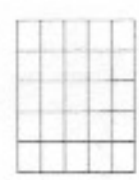
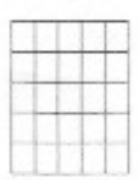
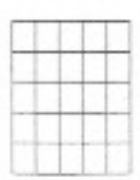
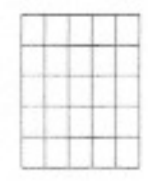
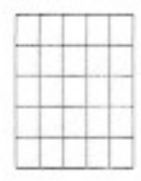
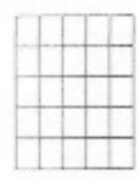
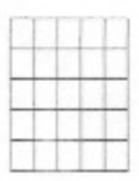
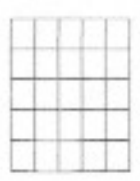
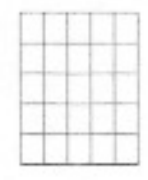
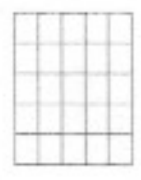
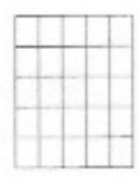
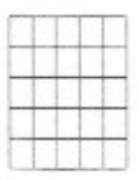
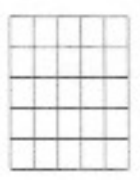
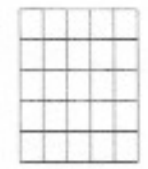
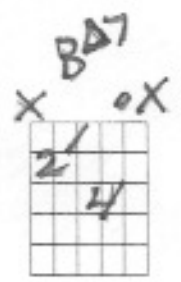
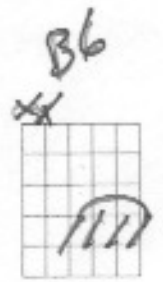
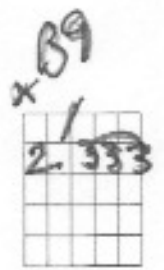
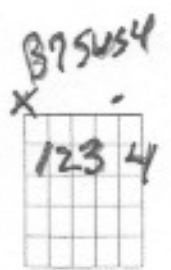
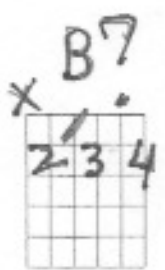
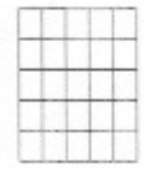
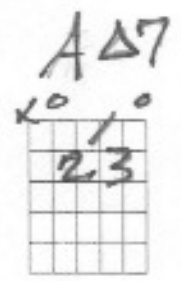
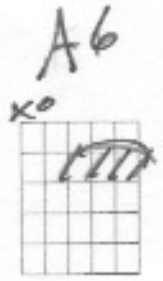
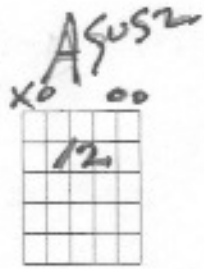
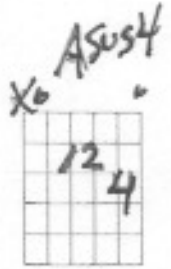
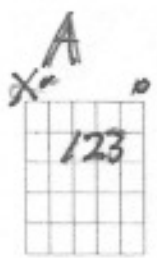
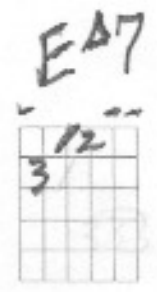
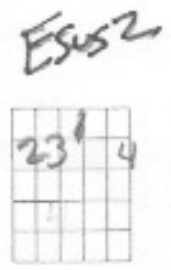
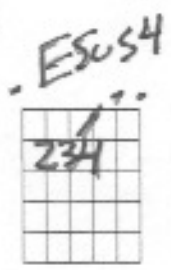
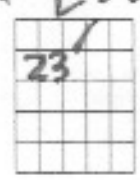
# Key of A

A X0A0 	ASUS4 X0A0 	ASUS2 X000 	A6 X0A0 	AΔ7 X0A0 	
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D XX0 	DSUS4 XX0 	DSUS2 XX0 0 	D6 XX0 	DΔ7 XX0 	
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E7 0000 	E7SUS4 0000 	E9 0000 	E6 0000 	EΔ7 XX00 	
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Key of E



Key of Em

Em  
1 2

Em sus2

Em sus2  
1 3

Em6

Em6  
1 3

Em7

Em7  
1 2 4

Em9

Em9  
1 2 3 4

Am

Am  
2 3

Am sus2

Am sus2  
2 4

Am6

Am6  
2 3 4

Am7

Am7  
2

Am9

Am9  
3

B7

B7  
2 3 4

B7 sus4

B7 sus4  
1 2 3

B9

B9  
2 3 3 3

B6

B6  
1 2 3 4

BΔ7

BΔ7  
1 3 2 4

Key of Am

Am  
2 3

Am sus2

Am sus2  
2 4

Am6

Am6  
2 3 4

Am7

Am7  
2

Am9

Am9  
3

Dm

Dm  
2 3

Dm sus2

Dm sus2  
2 4

Dm6

Dm6  
2 3 4

Dm7

Dm7  
2

Dm9

Dm9  
3

E7

E7  
2 3

E7 sus4

E7 sus4  
2 3

E9

E9  
2 3 4

E6

E6  
2 3 4

EΔ7

EΔ7  
1 3 3 3